

Docket No. 015275-60008

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of:

Fang LAI et al

Serial No. 09/972,469

Filed: October 5, 2001

For: INTEGRATED CIRCUIT WITH ON-
CHIP MEMORY AND METHOD FOR
FABRICATING THE SAME

)

) Group Art Unit: 1631

) Examiner: Carolyn L. Smith

) Confirmation No.: 4187

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Certificate of Transmission

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office (Fax No. (571) 273-8300) on December 6, 2005.
Name: Geraldine Mardox


STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Please advise, in writing, as to the current status of the above-identified application.

Respectfully submitted,

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